

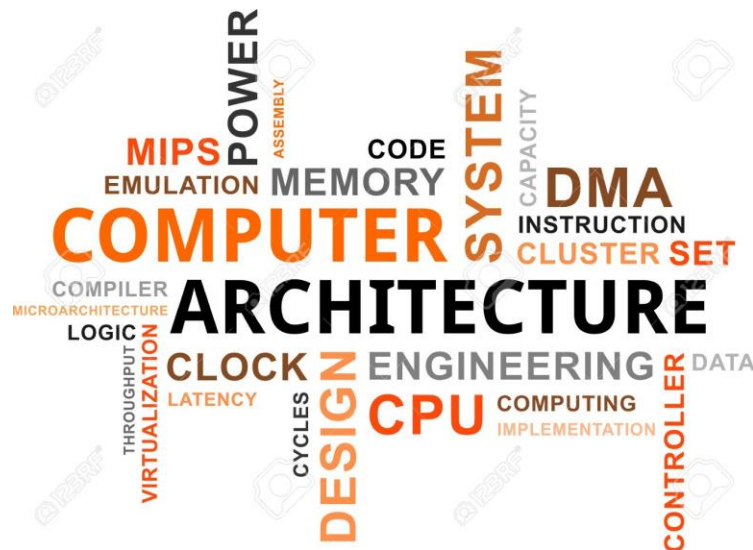
ECE 4480/5480: Computer Architecture and Design

Every SPRING

Instructor:	Dr. Byeong Kil Lee
Class hours:	Mon/Wed 1:40pm-2:55pm
Classroom:	ENGR 136
Office:	AEC 304
Office hours:	Mon/Wed: 3pm-4pm or by appointment
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Course Description: The design of large digital systems with emphasis on the computer. Architectural alternatives, instruction set design, implementations including microprogramming, and actual examples are discussed. Performance tradeoffs. Prerequisites: ECE 3430. College of Engineering students only. Meets with ECE 5480.

Topics to be Covered:



Prerequisites By topic:

- Basic logic design & State machine
- Basics of microcomputer system
- C/C++ programming (on Linux environment)
- Verilog modeling

Textbook:

Computer Organization and Design, 5th Edition
(Authors: D. Patterson and J. Hennessy), Morgan Kaufmann
[ISBN: 978-0-12-407726-3]

Grading:

- Homework & Lab assignments* (25%)
- Midterm Exam (20%)
- Final Exam (30%)
- Term Project (20%)
- Class Participation, Pop quizzes, etc. (5%)

*Homework and Lab Assignment (4 & 4): Lab assignments include C/C++, Python/Verilog programming, simulations, analysis, etc.